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L Number	Hits	Search Text	DB	Time stamp
1	48048	SRAM or (static adj5 random adj5 access adj5 memory)	USPAT; US-PGPUB; EPO; JPO;	2004/07/12 00:39
2	28	(SRAM or (static adj5 random adj5 access adj5 memory)) same (true adj3 bit\$1line)	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12 00:41
3	6	((SRAM or (static adj5 random adj5 access adj5 memory)) same (true adj3 bit\$lline)) same (continuous adj3 bit\$lline)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12 00:41
4	69	(SRAM or (static adj5 random adj5 access adj5 memory)) and (true adj3 bit\$1line)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12
5	6	((SRAM or (static adj5 random adj5 access adj5 memory)) and (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:41
6	6	("true bit line" or (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
7	16	("true bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:43
8	4	(("true bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12 00:43
9	11	("bit line" or (true adj3 bit\$1line)) same (continuous adj3 bit\$1line)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/07/12 00:43
10	27	("bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:44
11	4	(("bit line" or (true adj3 bit\$1line)) and (continuous adj3 bit\$1line)) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:44
12	75	bit\$1line and (complement adj3 bit\$1lin\$1) and pre\$1charg\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/12 00:45
14	68	(bit\$1line and (complement adj3 bit\$1lin\$1) and pre\$1charg\$3) and (FET or "field effect transistor" or transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/12 00:45

"field effect transistor" or transistor)	15	36		USPAT;	2004/07/12
and SRAM			bit\$1lin\$1) and pre\$1charg\$3) and (FET or	US-PGPUB;	00:46
and SRAM			"field effect transistor" or transistor))	EPO; JPO;	
16 32 (((bitslline and (complement adj3 bitsllins)) and presicharg\$3) and (FET or "field effect transistor" or transistor)) and SRAM) and 365/97.ccls. 17 5 (((bitslline and (complement adj3 bitsllins)) and presicharg\$3) and (FET or "field effect transistor" or transistor)) and SRAM) and 365/154.ccls. 18 9 (((bitslline and (complement adj3 bitsllins)) and presicharg\$3) and (FET or "field effect transistor" or transistor)) and SRAM) and 365/154.ccls. 18 9 (((bitslline and (complement adj3 bitsllins)) and presicharg\$3) and (FET or "field effect transistor" or transistor)) 0:29AT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;				DERWENT;	
16				IBM TDB	
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22 42 aipperspach-anthony-\$.in. 23 0 christensen-todd.in. 24 41 christensen-todd-\$.in. 25 0 freiburger-peter.in. 26 12 freiburger-peter-\$.in. 27 2004/07/12 00:47 28 2004/07/12 00:47 29 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48 2004/07/12 00:48					
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US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	23		christensen-todd.in.		2004/07/12
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26 12 freiburger-peter-\$.in. IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; DERWENT; DERWENT;					
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